IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

13281 U.S. PTO

BOX PATENT APPLICATION

The Commissioner of Patents and Trademarks P.O. Box 1450, Alexandria, Virginia 22313-1450

Sir:

Transmitted herewith for filing is a **new utility** patent application of: Chaur-Chin YANG et al.

Title of Invention: MULTI-CHIP STACK FLIP-CHIP PACKAGE

Enclosed are:

A specification and 17 claims.

Two (2) sheets of formal drawings (Figs. 1-3).

A Declaration and Appointment of Attorney

Claim for Priority and Priority Document

An Assignment to: Advanced Semiconductor Engineering, Inc.

Information Disclosure Statement along with PTO-1449 and Two (2) references.

The filing fee has been calculated as shown below:

		SMALL	LARGE
FOR: NO. FILED	NO. EXTRA	ENTITY	ENTITY
		RATE FEE	RATE FEE
BASIC FEE		<u>\$385.00</u>	<u> \$770.00</u>
TOTAL CLAIMS 17-20	= 0	\$ 9	\$18
INDEP CLAIMS 2- 3	= 0	\$ 43	\$86
MULTIPLE DEPENDENT	T CLAIMS	\$145.	\$290.
	TOTAL	Ś	\$770.00

- X A check in the amount of \$810.00 to cover the government filing fee and the assignment recording fee is enclosed.
- The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent application filing fees and processing fees under 37 CFR 1.16 and 37 CFR 1.17 or credit any overpayment to Deposit Account No. 501874.

Date: October 31, 2003

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